TC3-883

U.S. UTILITY Patent Application

PATENT NUMBER and ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10043276	01/14/2002	438		2812	

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**CONTINUING DATA VERIFIED: JML
none

** FOREIGN APPLICATIONS VERIFIED: JML
JAPAN 2001-6581 01/15/2001

PG-PUB DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed ☐ yes ☑ no 35 USC 119 conditions met ☐ yes ☐ no Verified and Acknowledged Examiner's initials JML		ATTORNEY DOCKET NO 040894-5755

TITLE : Semiconductor device and method for manufacturing the same

U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev 12-94)

NOTICE OF ALLOWANCE MAILED	Assistant Examiner	CLAIMS ALLOWED Total Claims / Print Claim for O.G
ISSUE FEE Amount Due / Date Paid	Primary Examiner	DRAWING Sheets Drwg. / Figs.Drwg. / Print Fig.
☐ TERMINAL DISCLAMER	PREPARED FOR ISSUE	Application Examiner

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